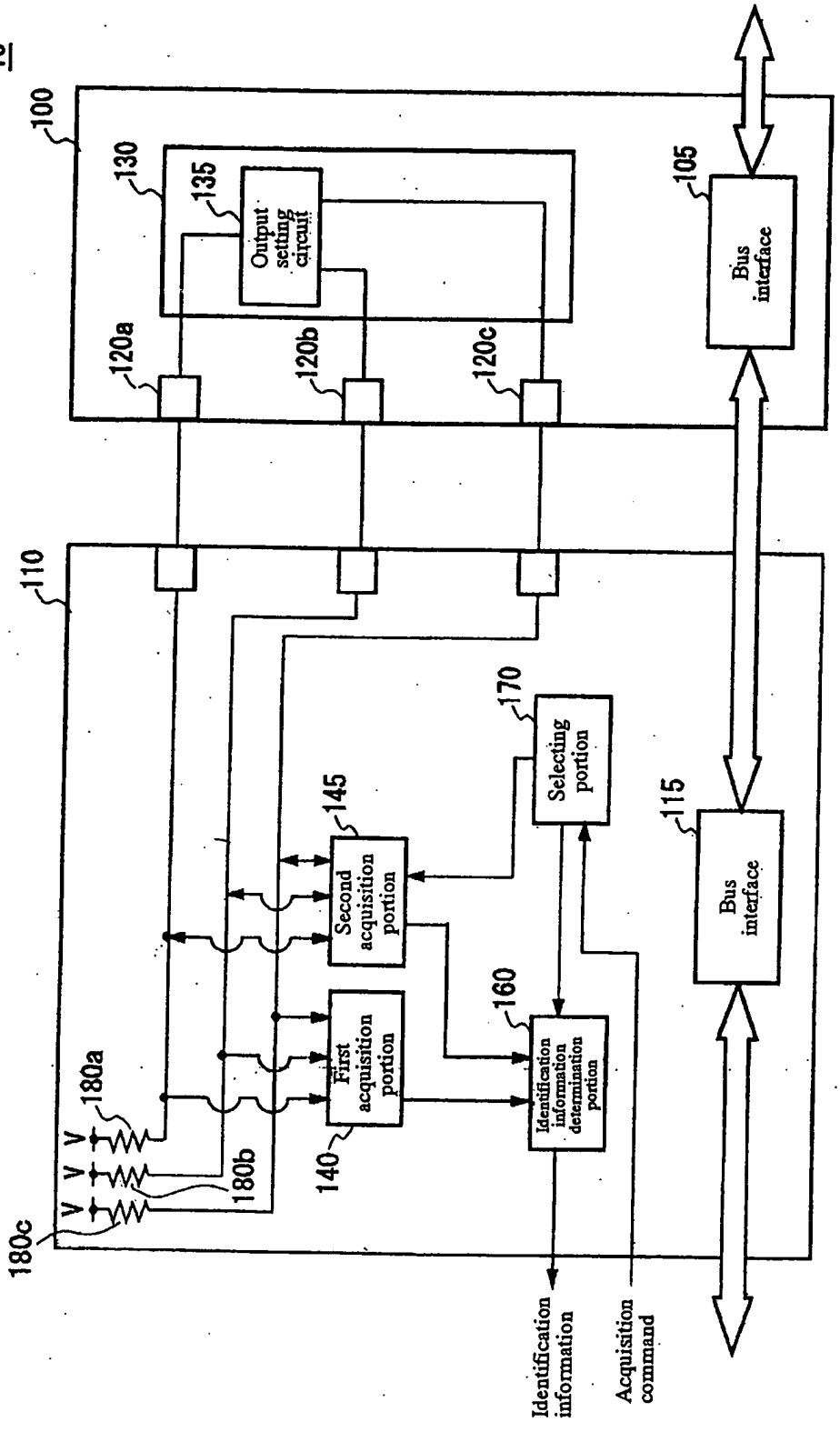
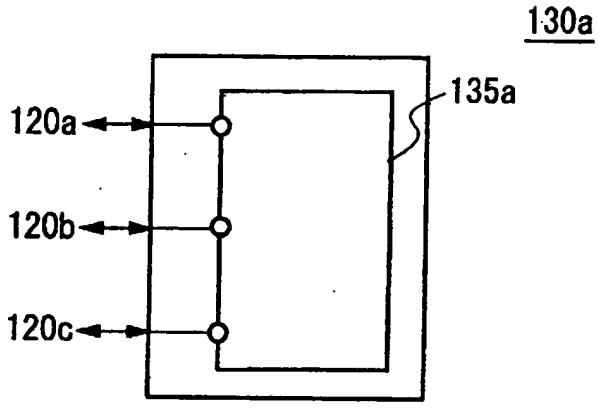
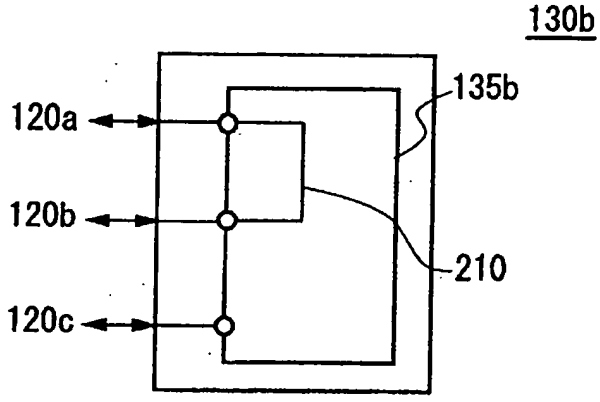

10
100
130
135
Output setting circuit
105
Bus interface
120a
120b
120c
110
170
Selecting portion
145
Second acquisition portion
115
Bus interface
160
Identification information determination portion
First acquisition portion
140
180a
180b
180c
V
Identification information
Acquisition command

Fig. 1

Fig. 2 (a)


130a
135a
120a
120b
120c

(b)


130b
135b
120a
120b
120c
210

(c)

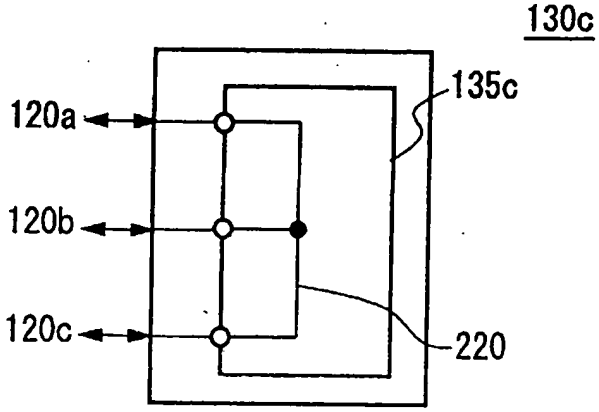
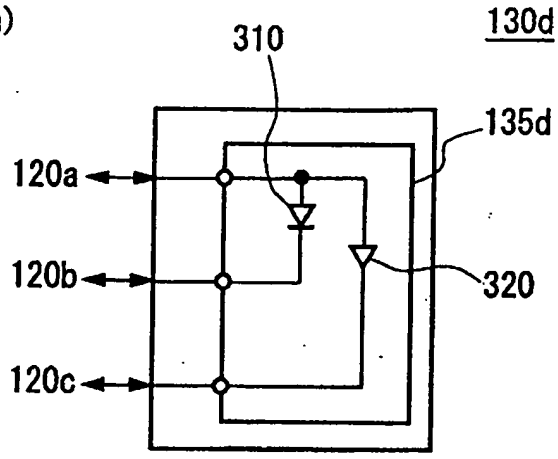
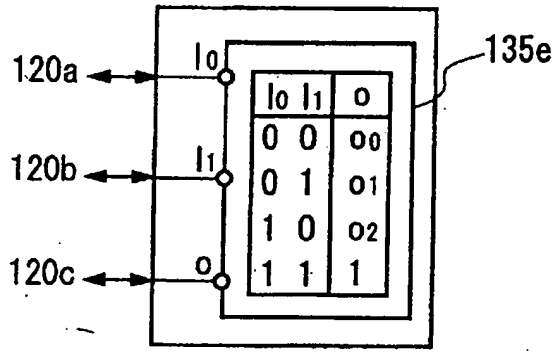

130c
135c
120a
120b
120c
220

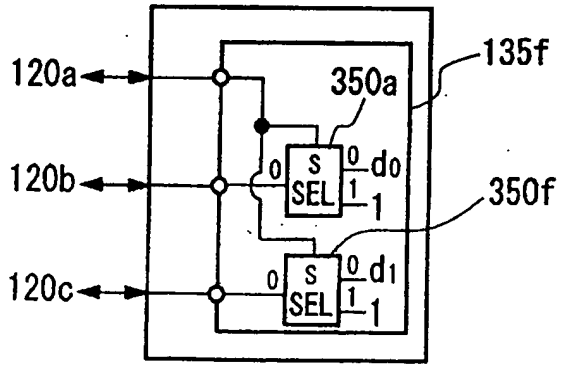
Fig. 3 (a)


130d
310
135d
120a
120b
120c
320

(b)


130e
135e
120a
120b
120c
I0
I1
O
I0 I1 O
0 0 O0
0 1 O1
1 0 O2
1 1 1

(c)


130f
135f
120a
120b
120c
350a
350f
S
SEL
0
1
d0
d1

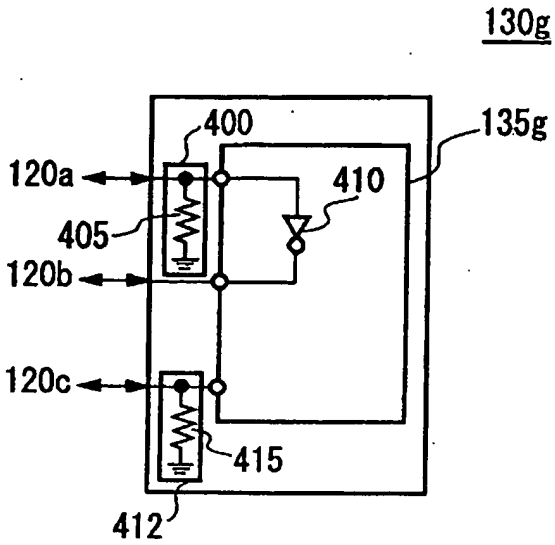


Fig. 4

Fig. 5 (a)

ID	b_2	b_1	b_0
0	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
4	1	0	0
5	1	0	1
6	1	1	0
7	1	1	1

TOTAL 8

0: Pull down

1: Pull up

(b)

ID	b_2	b_1	b_0
0	0	0	0
1	0	0	1
1a	0	#b0	1
1b	#b0	0	1
1c	#b0	#b0	1
2	0	1	0
2a	#b1	1	0
2b	0	1	#b1
2c	#b1	1	#b1
3	0	1	1
3a	#b0	1	1
3b	#b1	1	1
3c	0	b0	b1
3d	#b0	b0	b1
4	1	0	0
4a	1	0	#b2
4b	1	#b2	0
4c	1	#b2	#b2
5	1	0	1
5a	1	#b0	1
5b	1	#b2	1
5c	b0	0	b2
5d	b0	#b0	b2
6	1	1	0
6a	1	1	#b1
6b	1	1	#b2
6c	b1	b2	0
6d	b1	b2	#b1
7	1	1	1
7a	b1	b2	1
7b	b0	1	b2
7c	1	b0	b1
7d	b0	b2	b1

TOTAL 33

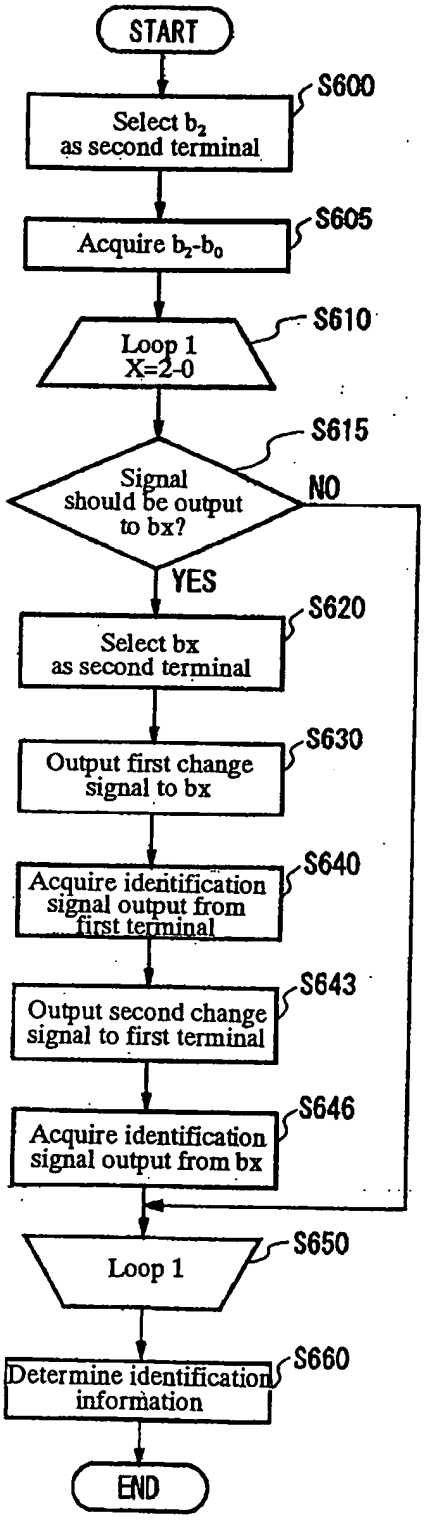
0: Pull down

1: Pull up

bx: Continuous with bx

#bx: Logical negation of bx

Fig. 6


START
S600
Select b_2 as second terminal
S605
Acquire b_2-b_0
S610
Loop 1
X=2-0
S615
Signal should be output to bx?
NO
YES
S620
Select bx as second terminal
S630
Output first change signal to bx
S640
Acquire identification signal output from first terminal
S643
Output second change signal to first terminal
S646
Acquire identification signal output from bx
S650
Loop 1
S660
Determine identification information
END

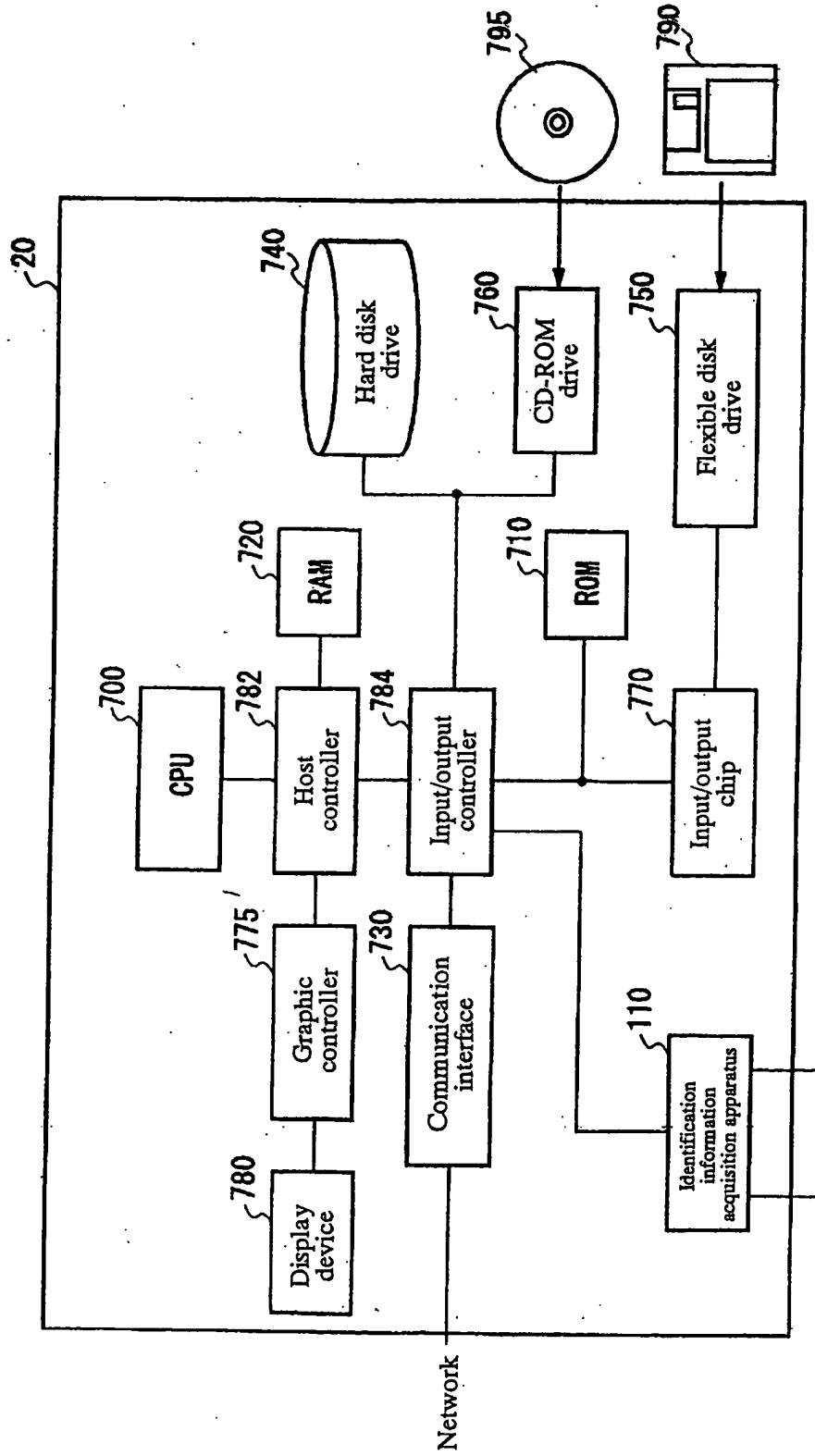

20
740
Hard disk drive
760
CD-ROM drive
750
Flexible disk drive
795
790
720
RAM
710
ROM
700
CPU
782
Host controller
784
Input/output controller
770
Input/output chip
775
Graphic controller
730
Communication interface
110
Identification information acquisition apparatus
780
Display device
Network
100b
100a

Fig. 7